

FIG 1



210

Receiving a register indicator corresponding to a register.

220

Accessing a memory cell based on the register indicator, wherein the memory cell and the register have the same bit capacity.

230

Transferring information between a memory cell and another component, wherein the information includes the same number of bits as the bit capacity of the memory cell.



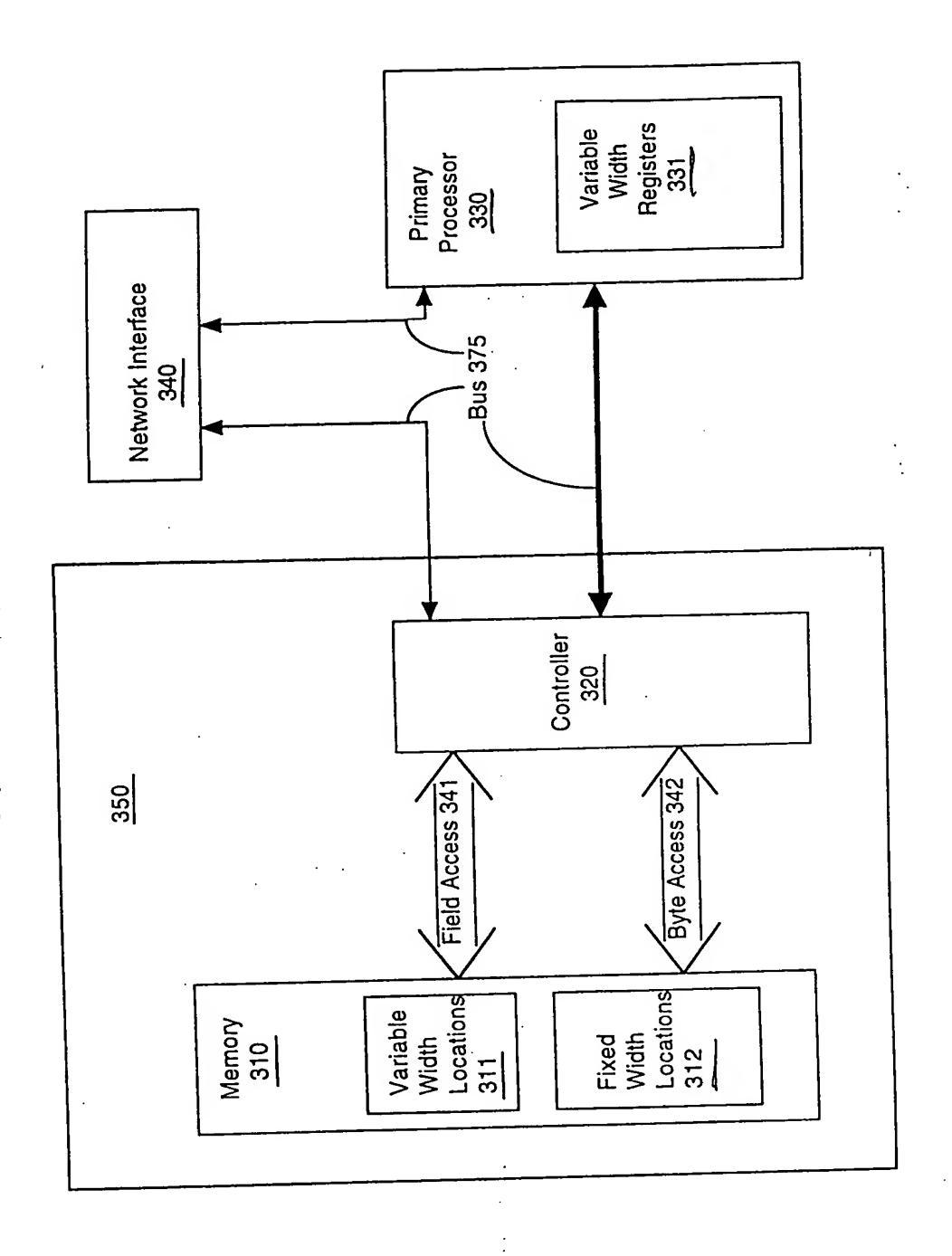


FIG 3A



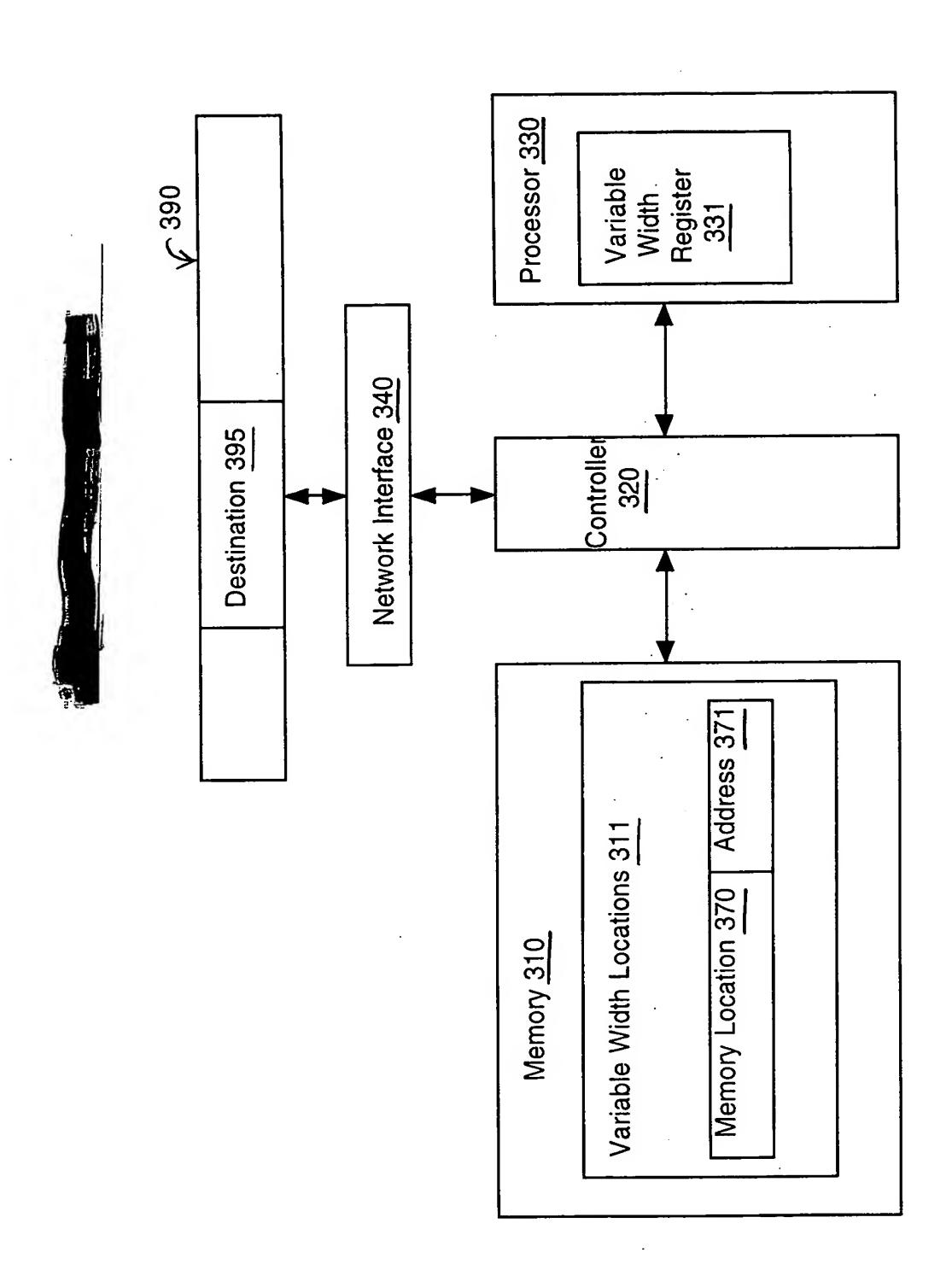
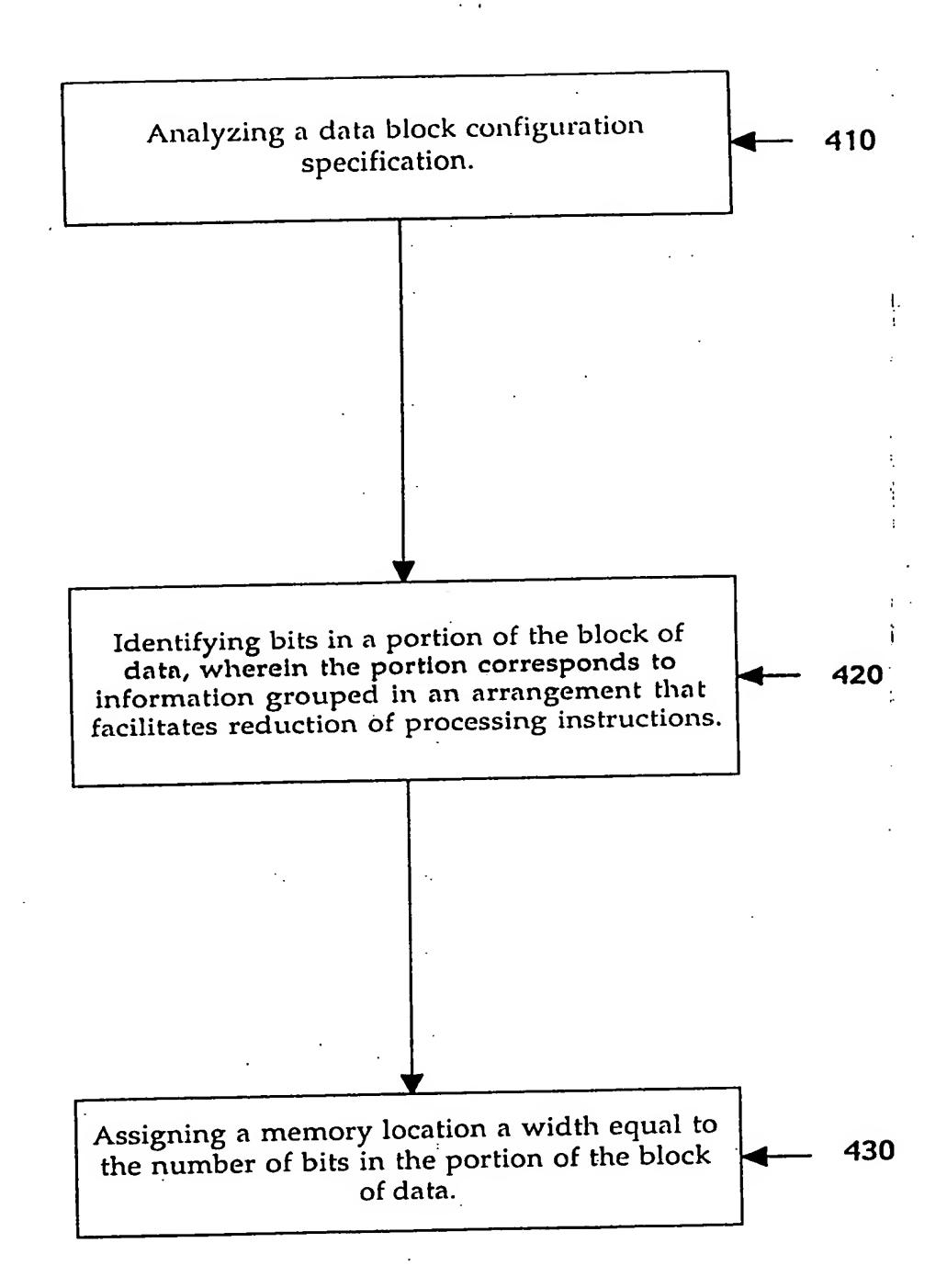


FIG 3B







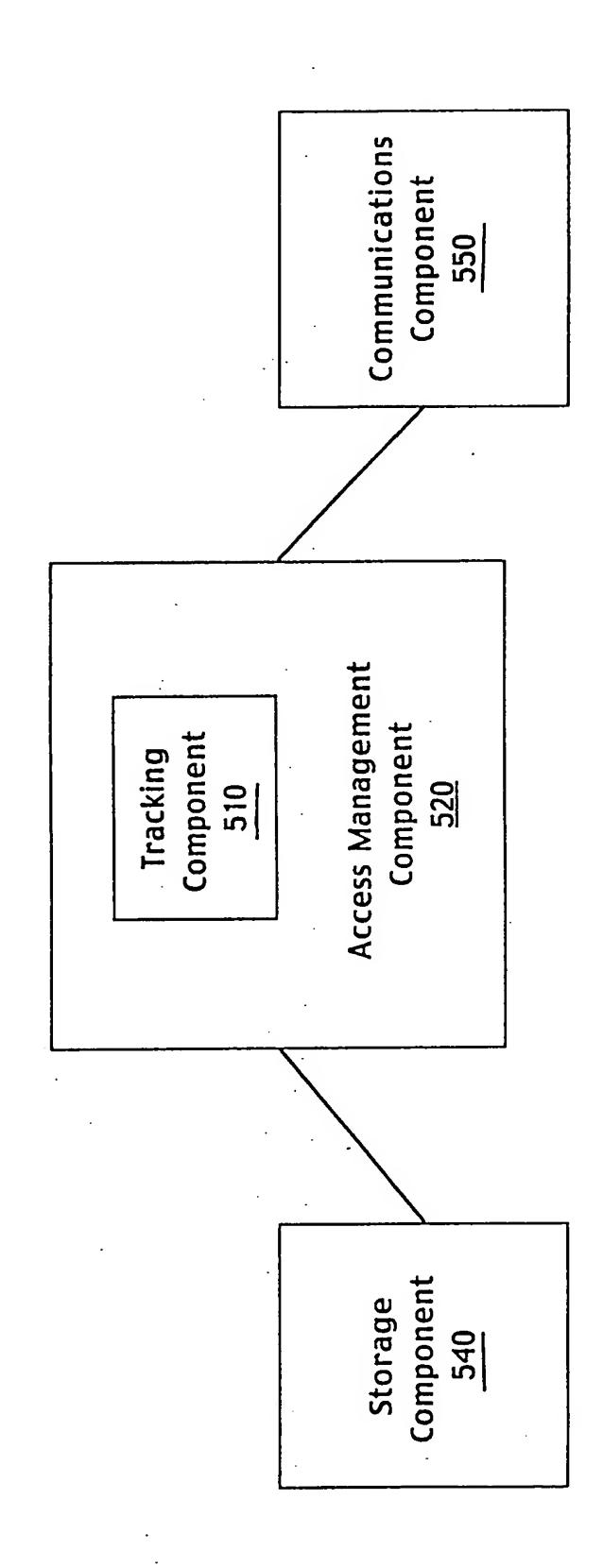


FIG. 5